	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Error
1	BRS	L3	0	1 and packetizing	USP AT	2003/02/17 14:34			0
2	BRS	L5	0	1 and packet adj1 assembler	USP AT	2003/02/17 14:31			0
3	BRS	L4	1	1 and packet adj1 assembling	USP AT	2003/02/17 14:31			0
4	BRS	L2	3	1 and packetization	USP AT	2003/02/17 14:33			0
5	BRS	L7	0	1 and cell adj1 assembler	USP AT	2003/02/17 14:34			0
6	BRS	L6	1	1 and frame adj1 assembler	USP AT	2003/02/17 14:34			0
7	BRS	L8	1	1 and packetize	USP AT	2003/02/17 14:35			0
8	BRS	L1	81	bit adj1 rate with available adj1 bandwidth	USP AT	2003/02/17 14:43			0
9	BRS	L9	458	bit adj1 rate with (bandwidth adj1	USP AT	2003/02/17 14:44			0
10	BRS	L10	1	bit adj1 rate adj1 determination with (bandwidth adj1	USP AT	2003/02/17 14:45			0
11	BRS	L11	0	bit adj1 rate adj1 determination with bandwidth adj1 (	USP AT	2003/02/17 14:45			0
12	BRS	L12	0	bit adj1 rate adj1 estimation with bandwidth adj1 (	USP AT	2003/02/17 14:45			0
13	BRS	L13	0	bit adj1 rate adj1 determination same bandwidth adj1 (	USP AT	2003/02/17 14:45			0
14	BRS	L14	1	bit adj1 rate adj1 determination and bandwidth adj1 (	USP AT	2003/02/17 14:46			0
15	BRS	L15	207	variable adj1 bit adj1 rate and bandwidth adj1 (assignment or allocation)	USP AT	2003/02/17 14:46			0
16	BRS	L16	27	variable adj1 bit adj1 rate same bandwidth adj1 (assignment or allocation)	USP AT	2003/02/17 14:52			0
17	BRS	L17	0	qos same adjustable adj1 bit adj1 rate	USP AT	2003/02/17 14:53			0
18	BRS	L18	0	qos same adjustible adj1 bit adj1 rate	USP AT	2003/02/17 14:53			0
19	BRS	L19	2	qos and adjustable adj1 bit adj1 rate	USP AT	2003/02/17 14:53			0
20	BRS	L20	2	service adj1 requirements and adjustable adj1 bit	USP AT	2003/02/17 14:54			0
21	BRS	L21	2	service adj1 requirements and adjusting adj1 bit	USP AT	2003/02/17 14:54			0